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Sheet 1

of 1

Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Perego
Art Unit	2188
Examiner Name	Verbrugge, K.
Attorney Docket Number	RAMB-01049US1

[illegible][illegible]Examiner
SignatureDate
Considered

2/15/05

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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Kevin Verheer

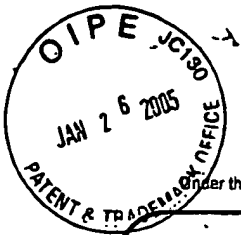
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PTO/SB/08b (08-03)
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

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First Named Inventor	Perego
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KW		IEEE Standard for Scalable Coherent Interface (SCI), "Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society", IEEE Std 1596-1992	
KW		SCI, "P1596: SCI, A Scalable Coherent Interface", November 28, 1988	
KW		GUSTAVSON et al., DRAFT "The Scalable Coherent Interface Project (SuperBus)", SCI, August 22, 1988, Rev. 14	
KW		SCHANKE, "Proposal for Clock Distribution in SCI", SCI, May 5, 1989	
KW		JAMES, "Scalable I/O Architecture for Buses", COMPCON Spring 1989, SCI, November 28, 1988	
KW		VOLZ et al., "Position Paper On Global Clock For the Futurebus +", SCI, 1989	
KW		KRISTIANSEN et al., "Scalable Coherent Interface", to appear in Eurobus Conference Proceedings, May 1989	

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